

FIG. 1

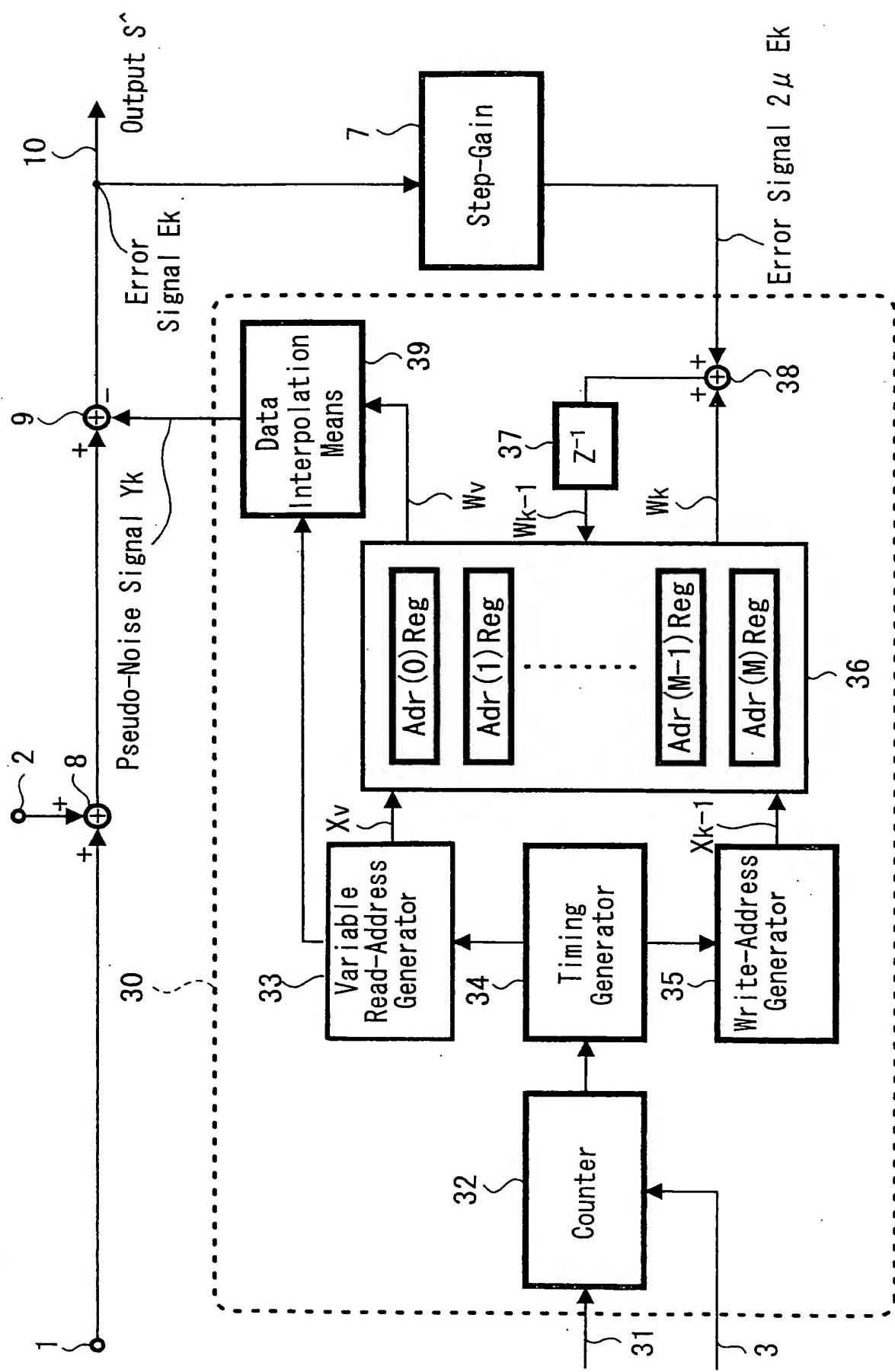


FIG. 2

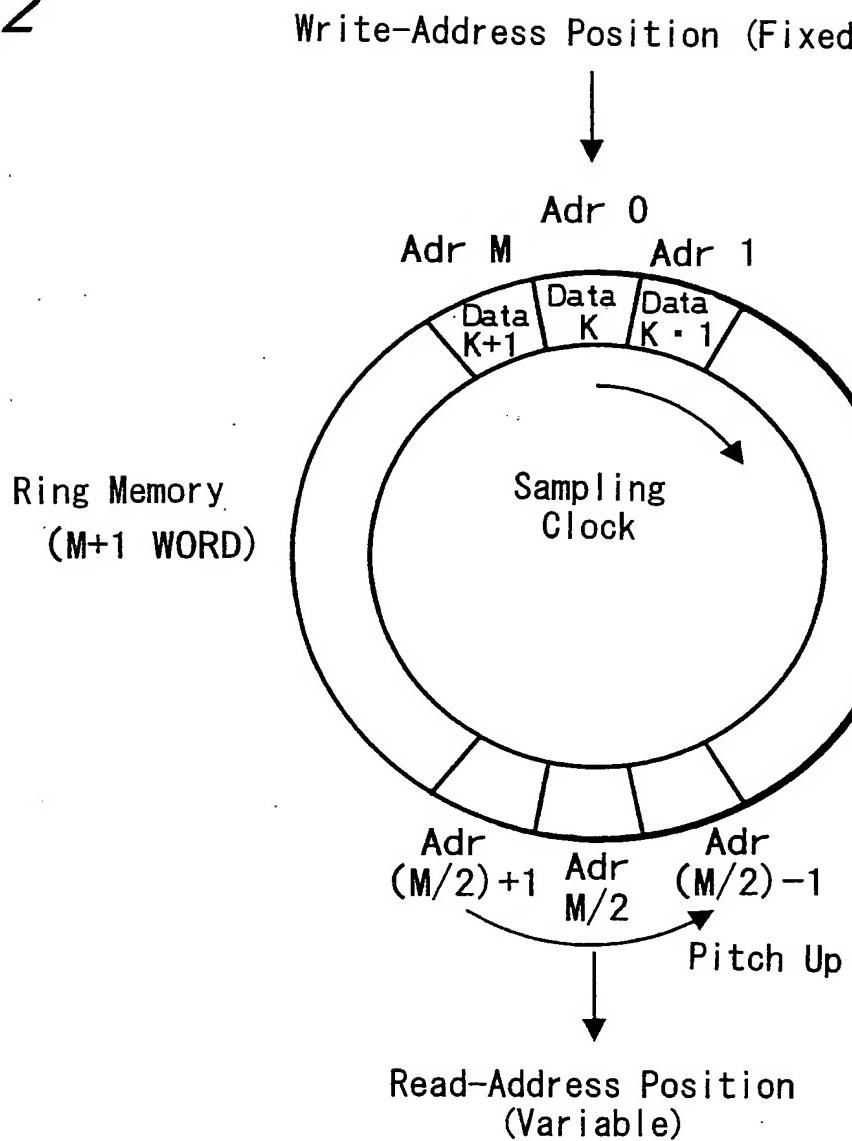


FIG. 3A

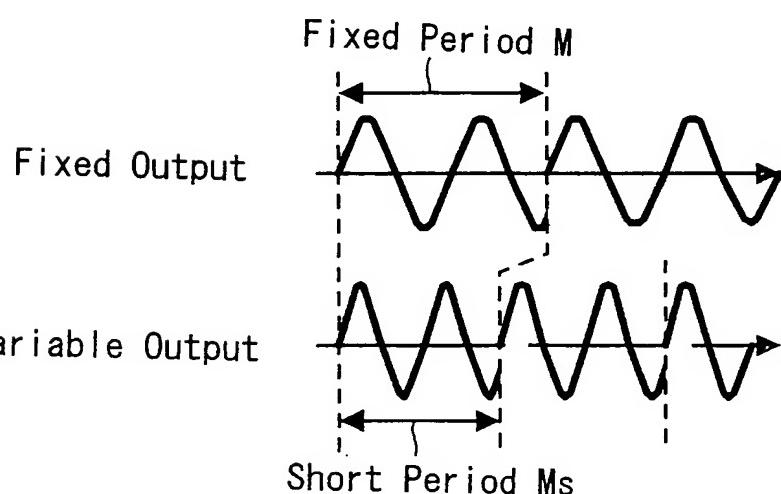
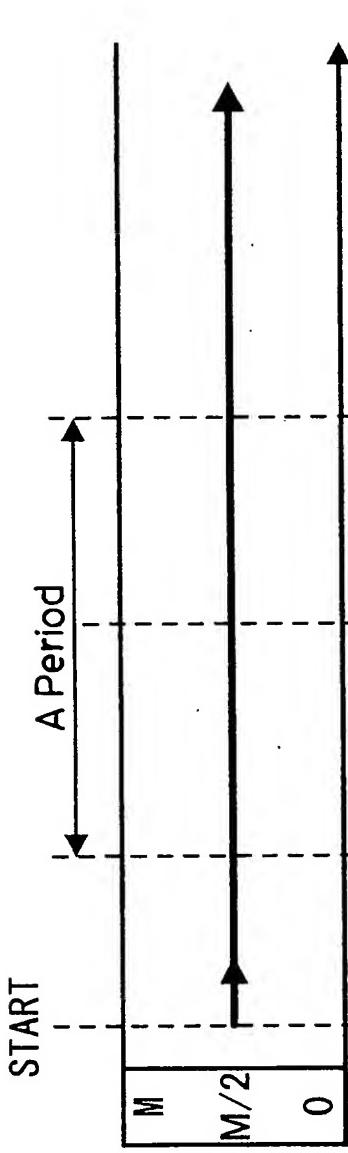


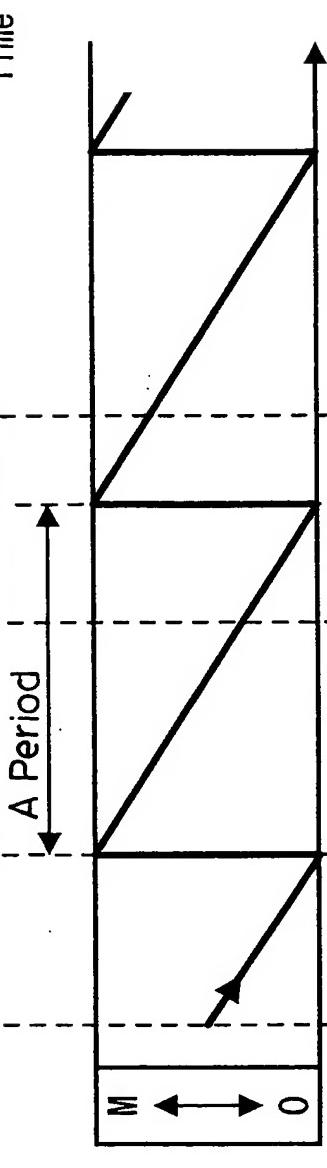
FIG. 3B

FIG. 4A



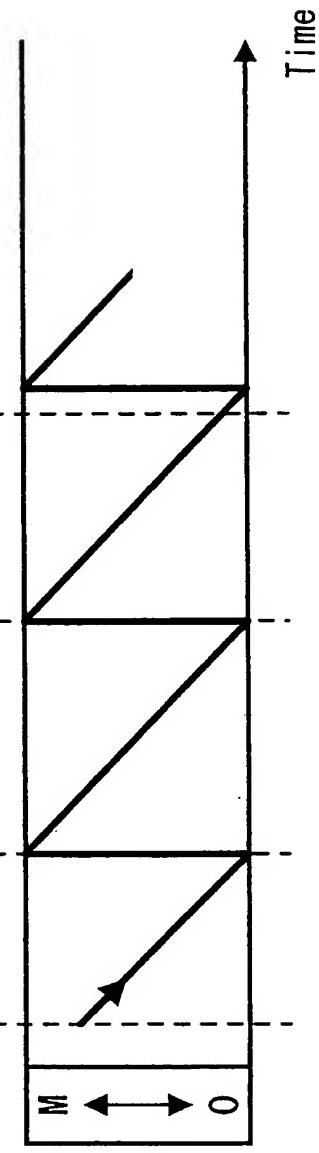
Fixed Read Addressing

FIG. 4B



Variable Read Addressing

FIG. 4C



Variable Read Addressing

FIG. 5

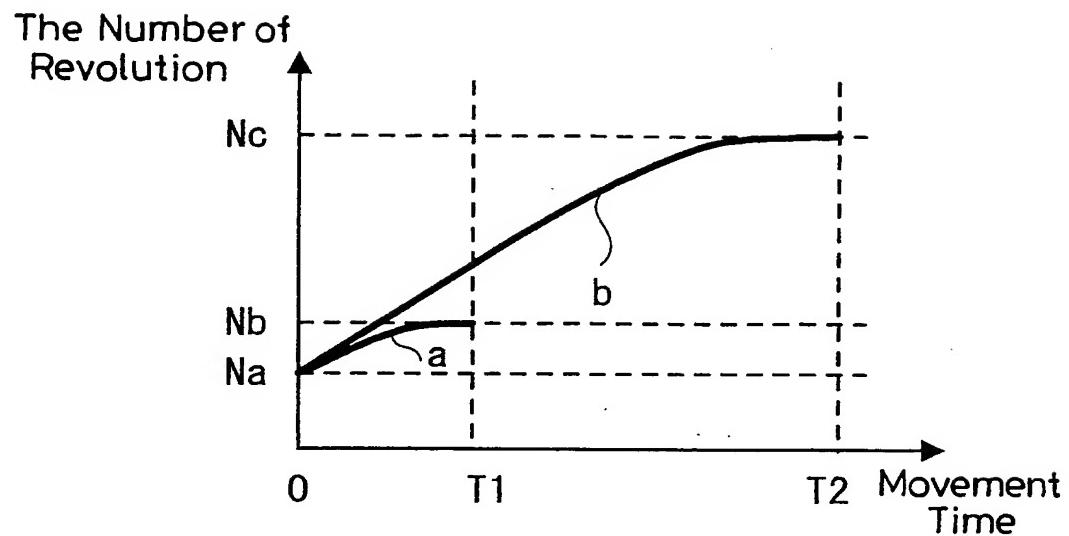


FIG. 6

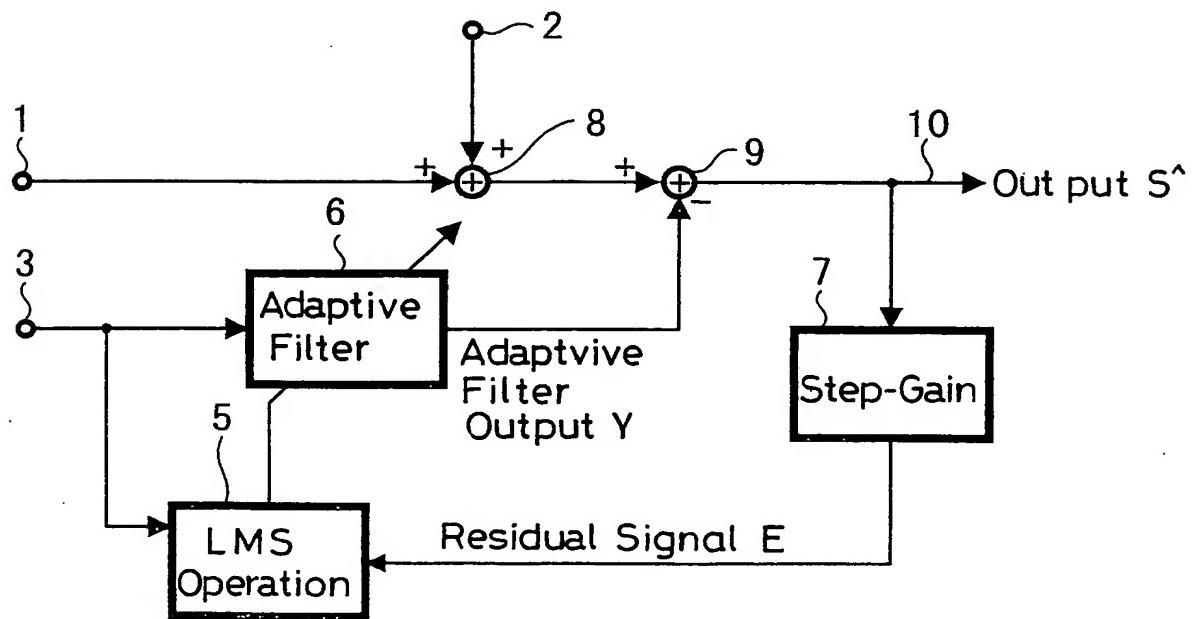
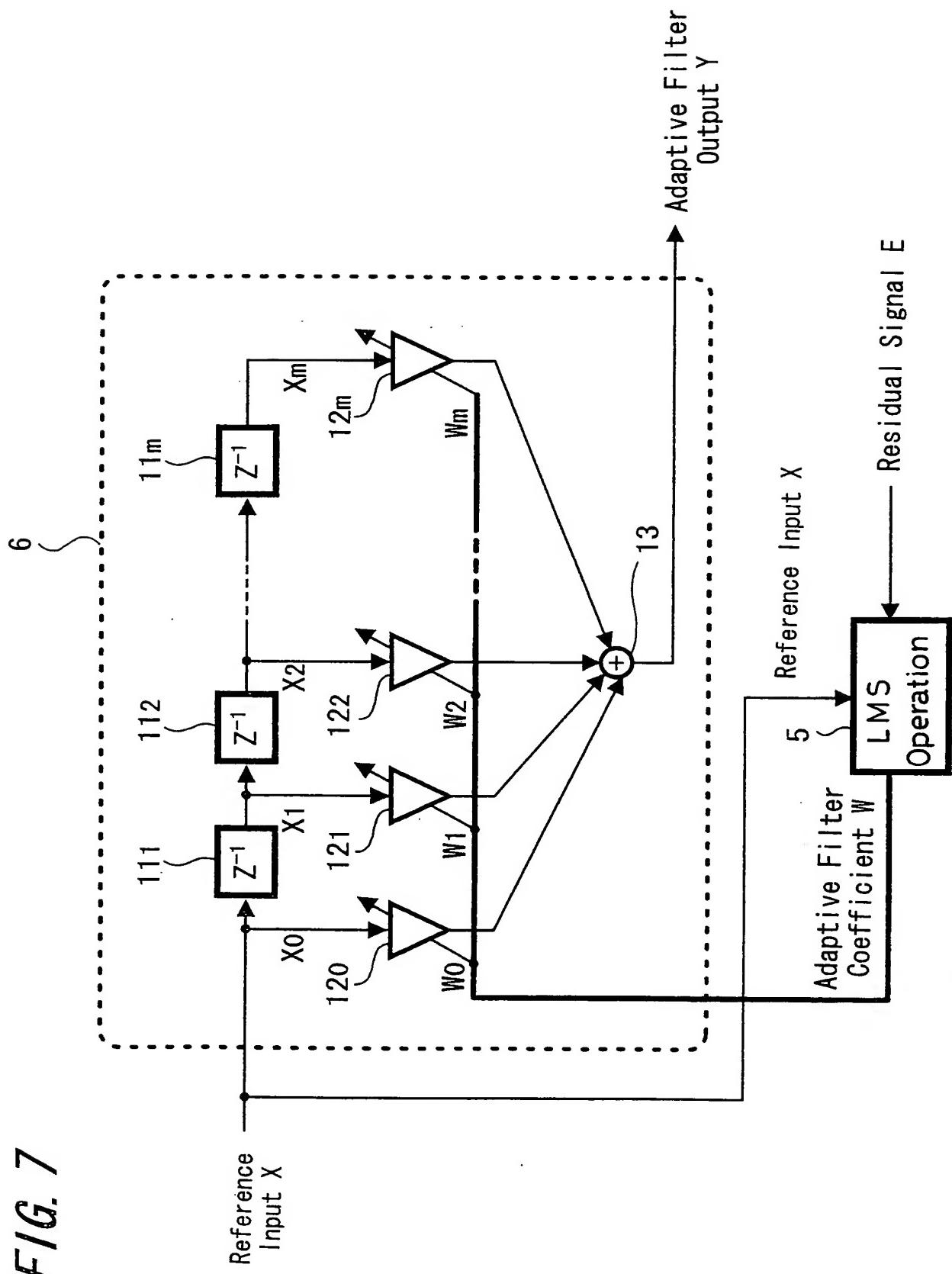


FIG. 7



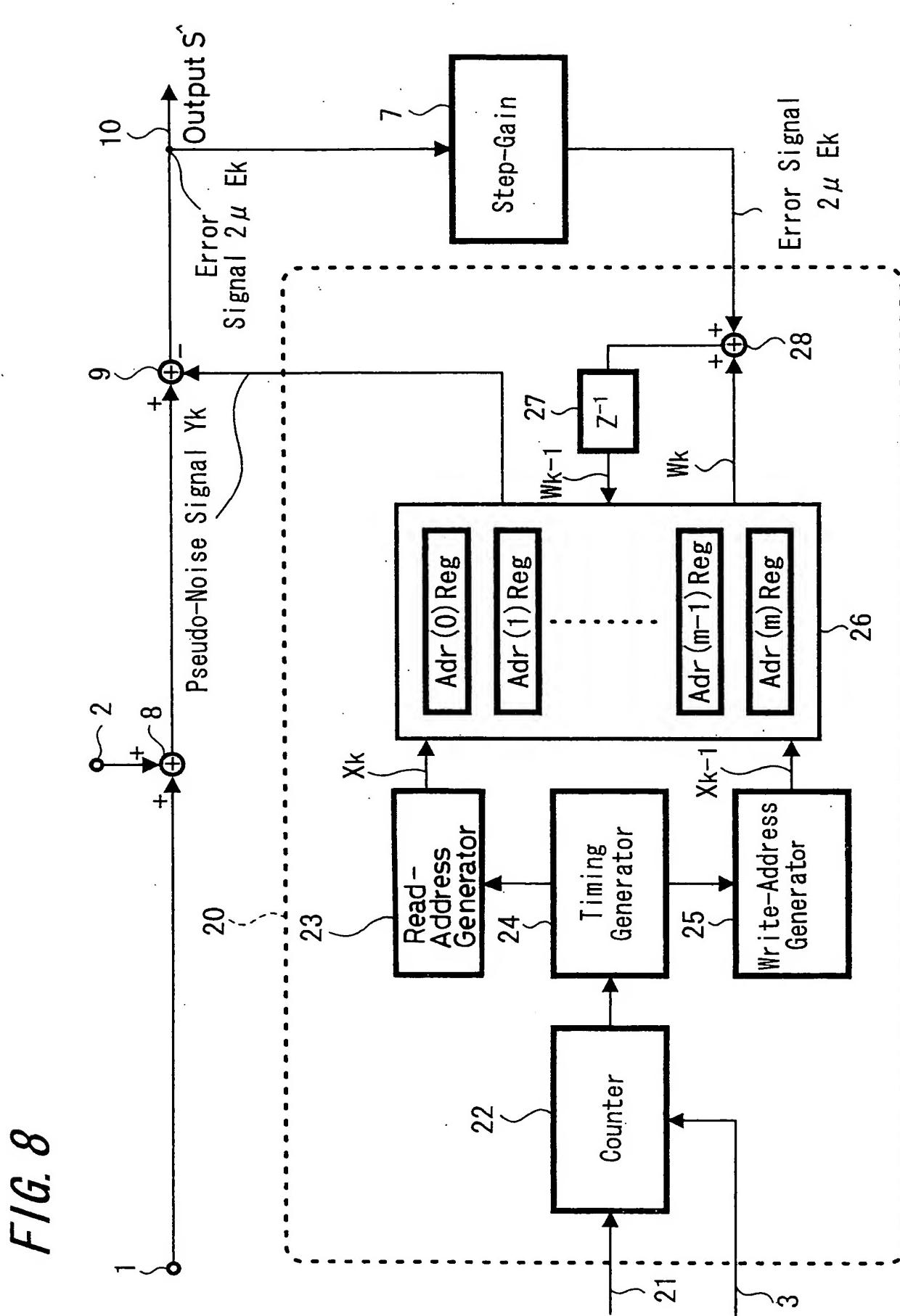


FIG. 9

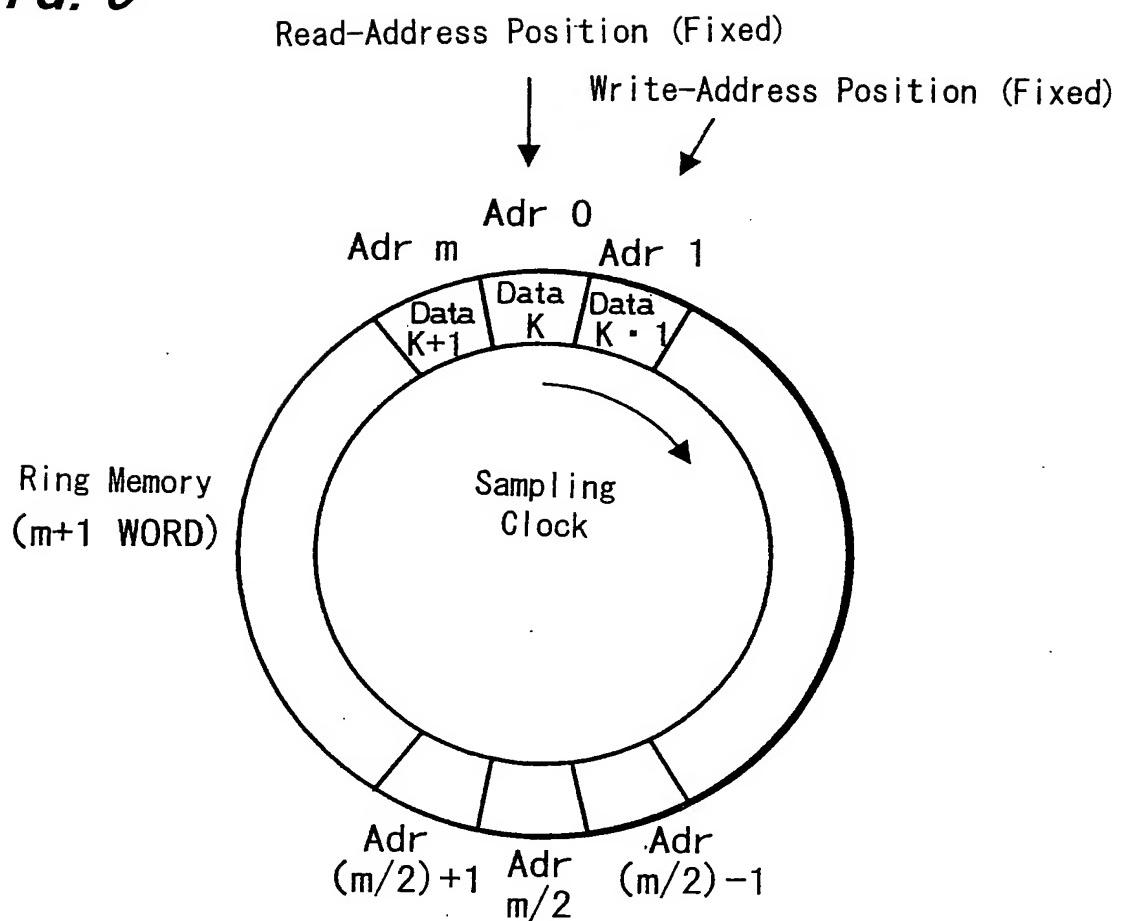


FIG. 10A

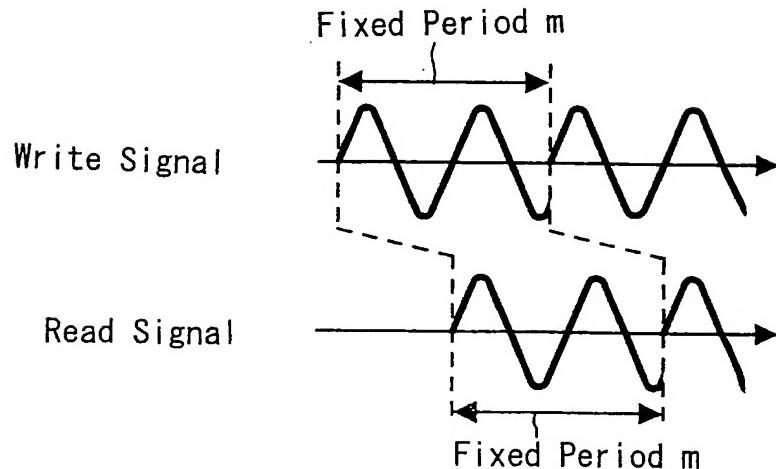


FIG. 10B

DESCRIPTION OF REFERENCE NUMERALS

- 1 INPUT TERMINAL TO WHICH MAIN AUDIO INPUT S IS SUPPLIED
- 2 INPUT TERMINAL TO WHICH NOISE N IS SUPPLIED
- 3 INPUT TERMINAL TO WHICH VARIABLE PERIOD PULSE SIGNAL IS
SUPPLIED AS REFERENCE INPUT SIGNAL
- 7 STEP GAIN
- 8, 9 ADDER
- 10 ... OUTPUT TERMINAL
- 30 ... ADAPTIVE SIGNAL PROCESSOR
- 31 ... INPUT TERMINAL TO WHICH SAMPLING SIGNAL IS SUPPLIED
- 32 ... COUNTER
- 33 ... VARIABLE READ ADDRESS GENERATOR
- 34 ... TIMING GENERATOR
- 35 ... WRITE ADDRESS GENERATOR
- 36 ... ACCUMULATOR
- 37 ... UNIT SAMPLE TIME DELAY
- 38 ... ADDER
- 39 ... DATA INTERPOLATION MEANS